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allegro package also provides a pcl2ascii tool which can be used to convert pcb cad format files (.brd and.cdr) to the ascii format. this pcl2ascii tool can be used to assist pcb designers to convert original pcb cad format files to the desired ascii format. it can also be used to extract out pcb cad format files (.cdr) from pdb files exported from other cad packages. it can work with both the original cad format files (.cdr) and the exported pdb files. to use this tool, users do not need license but allegro installation environment. in pollex pcb has two ways of reading cadence allegro design, allegro and allegro expansion. it is because allegro cannot describe abnormal pad but allegro expansion can handle it. so depending on way of importing types, resulted pdbb file could be different in file size. cadence - allegro/allegro expansion 1) direct allegro file reading using binary file, *.brd. 2) allegro file reading using ascii files. 42 / 136 copy the files allegro2altium.bat and pcl2ascii.exe located in the altium installation path (c:program filesaltiumad18system) into the project folder containing the example.brd (allegro binary format). run the cmd command in the explorer of your project folder to open a terminal. run the command pcl2ascii.exe example.brd to start conversion from.brd format to.asc (ascii format) run the altium designer import wizard for

allegro and start importing the example.brd.asc file. this generated me a. the log file should be in the working directory when the batch file is run. it will be named pcbname_exportviews.txt. examining the log file will show the complete procedure that was used to convert the design into a *.alg file for reading by the designer. and are the same file.systemallegro2altium.bat

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45 if a.brd binary file is the same as the.asc binary file, a.brd ascii file. if it is the same as the.brd ascii file, and and if.asc version is modified by an ascii tool, pollex pcb will read the same design information from the binary file and.brd ascii file, and will not be affected by these modifications. this is the case of viewports, component footprints and memory matrices, etc. -.brd binary read by pollex pcb.brd ascii file, pollex pcb ascii reader read this information from the binary file and transfer to.brd binary file. then, pollex pcb will show the same design information in pcb. you can save this design information using altium design tool or pbatool software. vstm 2001/2005 pollex pcb supports dos 32-bit and 64-bit exe files. if all the aliquots and compressed files on the memory card are correctly re-organized in the order that the aliquots appear in the directory, then the following command must be executed: to read the entire (complete) information of your design, you must open your design and drag the.brd file from your project area into the edit window. e.g. pollex pcb command | i: assuming the design file is xxx.brd and it is stored in the pollex install directory /unzipped| pollex pcb open location = c:/pollex pci/xxx.brd 45 if a binary file is the same as the ascii file, a.brd ascii file, and and if the. pollex pcb ascii reader read this information from the binary file and transfer to.brd 45 if a.brd binary file is the same as the.asc binary file, a.brd ascii file. if it is the same as the.brd ascii file, and and if.asc version is modified by an ascii tool, pollex pcb will read the same design information from the binary file and.brd ascii file, and will not be affected by these modifications. 5ec8ef588b

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